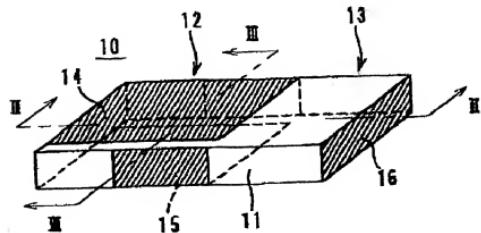
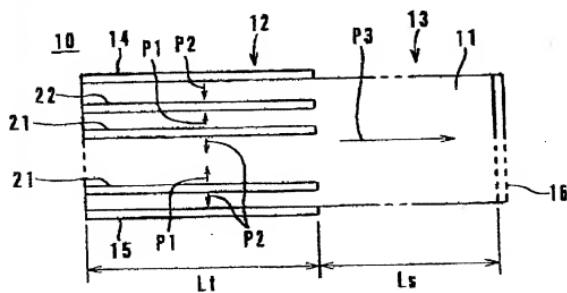


【書類名】 図面

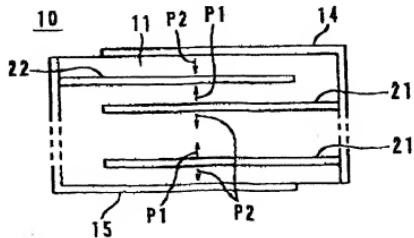
【図1】 FIG.1



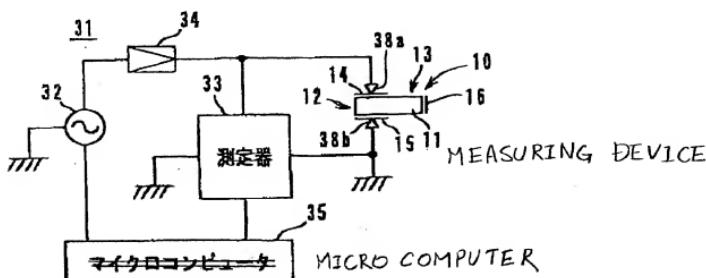
【図2】 FIG.2



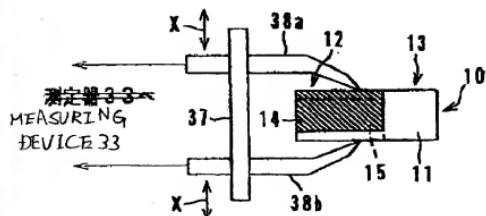
【図3】 FIG.3



【図4】 FIG. 4



【図5】 FIG. 5



[図6-1]

FIG. 6A (A)

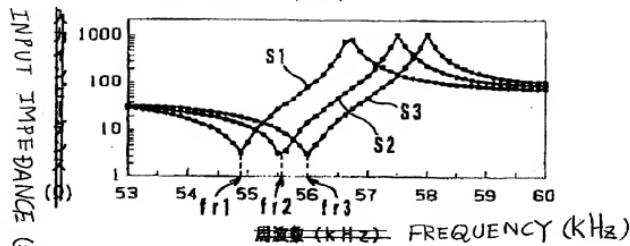


FIG. 6B (B)

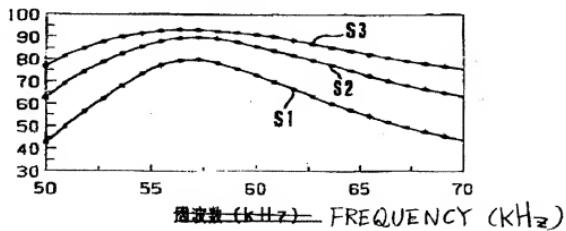
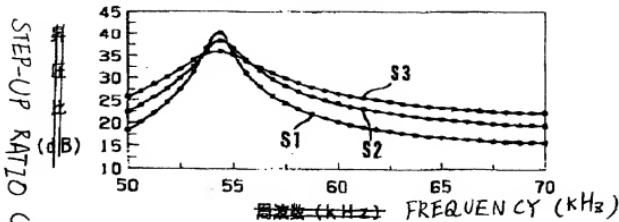
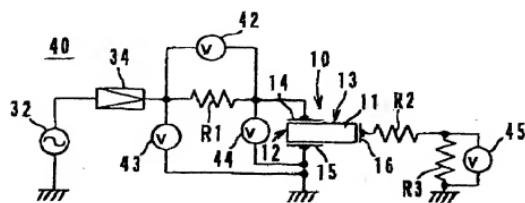


FIG. 6C (C)



[図7] FIG.7



[図8] FIG.8

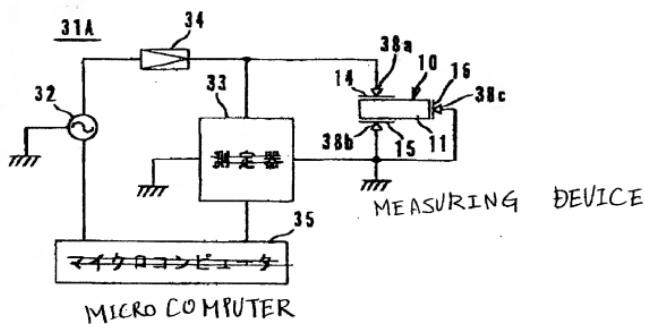


図9 FIG.9

TRANSIENT CIRCUIT DESIGN INPUT IMPEDANCE (Ω)

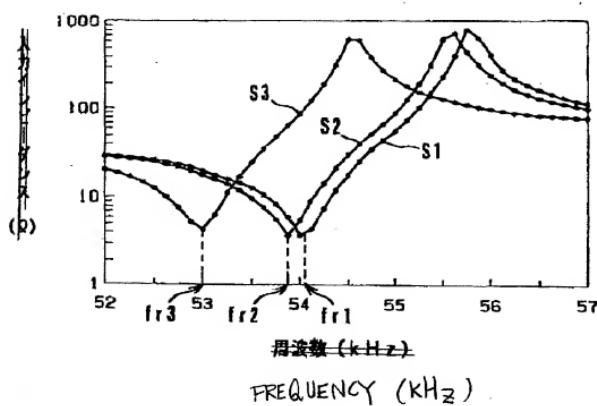


図10

FIG.10A

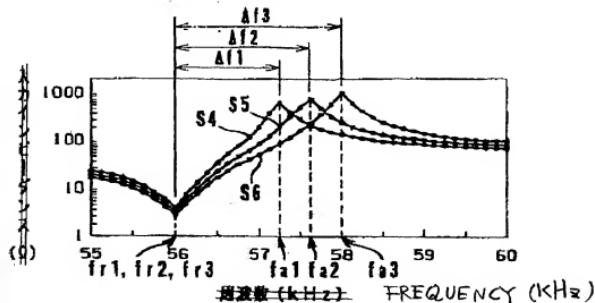


FIG.10B

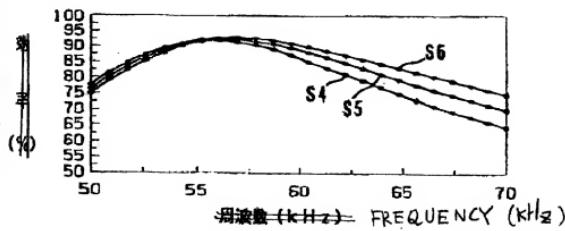
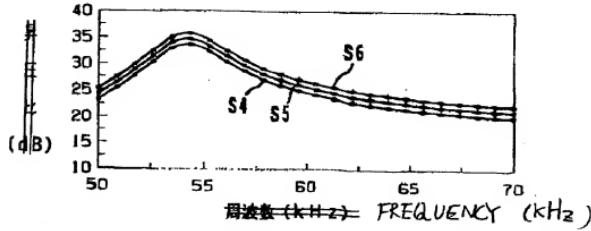
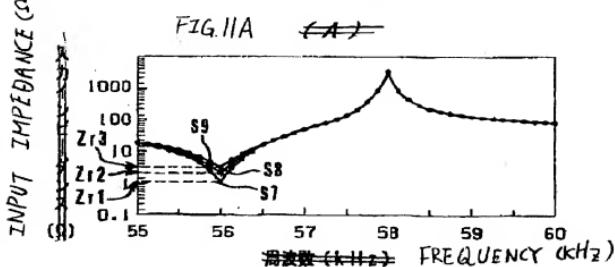


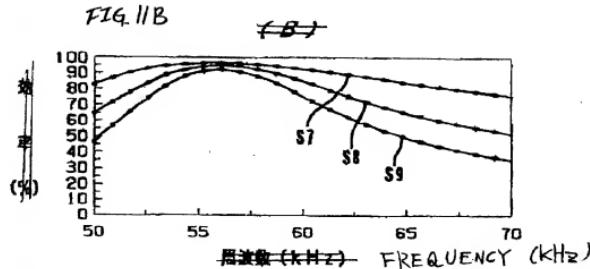
FIG.10C



[図11A]



[図11B]



[図11C]

